

What is claimed is:

1. A plasma display panel comprising:

5 a plurality of pairs of display electrodes, a pair of the plurality of pairs of display electrodes which are disposed parallel to each other on a front substrate and form a discharge gap for emitting light for display;

a dielectric layer, which is formed on the front substrate and covers the plurality of pairs of display electrodes excluding at least a part of the discharge gap; and

10 a data electrode disposed on a rear substrate, which is placed facing the front substrate across discharge space, in a manner to cross under the display electrodes.

2. The plasma display panel of claim 1,

15 wherein a thickness of the dielectric layer in a direction where the pair of display electrodes face each other is not larger than that in a direction where the pair of display electrodes face the rear substrate.

3. The plasma display panel of claim 1,

20 wherein a float electrode is disposed at the discharge gap, which is formed on the front substrate.

4. The plasma display panel of claim 3,

wherein the float electrode is transparent for visible light.

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5. The plasma display panel of claim 4,

wherein the float electrode is formed of at least one of a narrow line and

a combination of narrow lines.